

1990

**U.S. UTILITY Patent Application**

**G.L.P.E.**

**PATENT DATE**

SCANNED

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART. UNIT	EXAMINER
09/915396	D F	438	689	1765	UPK

APPLICANTS  
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TITLE  
Method of manufacturing semiconductor device

FTD 2040  
1288

[illegible]

 <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg	Figs. Drwg	Print Fig	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date) _____			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____	_____ (Primary Examiner) _____ (Date) _____			<b>ISSUE FEE</b>	
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